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Preliminary

TO : Dell / Quanta

DATE : Mar. 5, 2004.

SAMSUNG TFT-LCD

MODEL NO. : LTN141XB-L04

NOTE :

The information described in this SPEC is preliminary and can be changed without prior notice.

APPROVED BY : K. H. Shin

PREPARED BY : **LCD Application Engineering Group 1, TCS Team**

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REVISION HISTORY

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Date	Revision No.	Page	Summary
Feb. 24. 2004	P00	All	LTN141XB-L04 Model spec was issue first.
Mar. 05. 2004	P01	5	Shock & Vibration conditions are changed.
		6	Min value of lamp frequency is corrected.
		13	Min duty is changed to 20%.

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GENERAL DESCRIPTION

DESCRIPTION

LTN141XB-L04 is a color active matrix TFT (Thin Film Transistor) liquid crystal display (LCD) that uses amorphous silicon TFT as a switching devices. This model is composed of a TFT LCD panel, a driver circuit and a backlight unit. The resolution of a 14.1" contains 1024 x 768 pixels and can display up to 262,144 colors. 6 O'clock direction is the Optimum viewing angle.

FEATURES

- High contrast ratio, high aperture structure
- XGA (1024 x 768 pixels) resolution
- Low power consumption
- Fast Response
- Single CCFL
- DE(Data enable) only mode
- 3.3V LVDS Interface
- Onboard EEDID chip
- SPWG Style " B" Design
- Attached Burst mode Inverter

APPLICATIONS

- Notebook PC
- If the usage of this product is not for PC application, but for others, please contact SEC

GENERAL INFORMATION

Item	Specification	Unit	Note
Display area	285.7(H) x 214.3(V) (14.1" diagonal)	mm	
Driver element	a-Si TFT active matrix		
Display colors	262,144		
Number of pixel	1024 x 768	pixel	
Pixel arrangement	RGB vertical stripe		
Pixel pitch	0.279(H) x 0.279(V) (TYP.)	mm	
Display Mode	Normally white		
Surface treatment	Haze 25, Hard-Coating 3H		

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Mechanical Information

Item		Min.	Typ.	Max.	Unit	Note
Module size	Horizontal (H)	298.5	299.0	299.5	mm	w/o Inverter ass' y
	Vertical (V)	227.5	228.0	228.6	mm	
	Depth (D)	-	-	5.5	mm	Inverter assembly (1), (2)
Weight		-	420	440	g	LCD module only
		-	435	455	g	w/ Inverter assembly

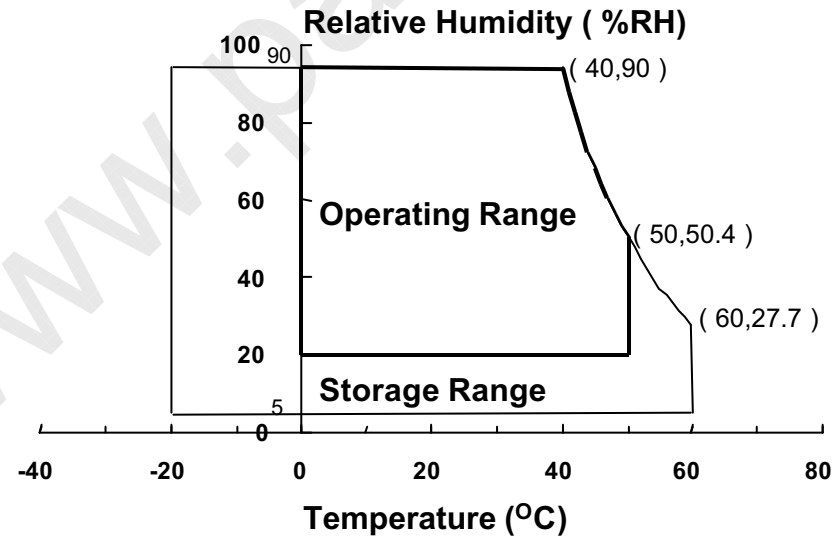
Note (1) Measurement condition of outline dimension
. Equipment : Bernier Calipers
. Push Force : 500g ·f (minimum)
(2) User Hole Torque : (2.0±0.5) kg·f·cm, 5 times

1. ABSOLUTE MAXIMUM RATINGS

1.1 ENVIRONMENTAL ABSOLUTE RATINGS

Item	Symbol	Min.	Max.	Unit	Note
Storage temperate	TSTG	-20	60	°C	(1)
Operating temperate (Temperature of glass surface)	TOPR	0	50	°C	(1)
Shock (non-operating)	Snop	-	240	G	(2),(4)
Vibration (non-operating)	Vnop	-	2.41	G	(3),(4)

Note (1) Temperature and relative humidity range are shown in the figure below.
95 % RH Max. (40 °C ≥ Ta)
Maximum wet - bulb temperature at 39 °C or less. (Ta > 40 °C) No condensation



(2) 2ms, half sine wave, one time for ±X, ±Y, ± Z.
(3) 5 - 500 Hz, random vibration, 30min for X, Y, Z.
(4) At testing Vibration and Shock, the fixture in holding the Module to be tested have to be hard and rigid enough so that the Module would not be twisted or bent by the fixture.

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1.2 ELECTRICAL ABSOLUTE RATINGS

(1) TFT LCD MODULE

V_{DD} =3.3V, V_{SS} = GND = 0V

Item	Symbol	Min.	Max.	Unit	Note
Power Supply Voltage	V_{DD}	VSS - 0.3	3.6	V	(1)

Note (1) Within Ta (25 ± 2 °C)

(2) BACK-LIGHT UNIT

Ta = 25 ± 2 °C

Item	Symbol	Min.	Max.	Unit	Note
Lamp Current	I_L	2.0	7.0	mArms	(1)
Lamp frequency	F_L	40	80	kHz	(1)

Note 1) Permanent damage to the device may occur if maximum values are exceeded
Functional operation should be restricted to the conditions described under normal operating conditions.

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2. OPTICAL CHARACTERISTICS

The following items are measured under stable conditions. The optical characteristics should be measured in a dark room or equivalent state with the methods shown in Note (5).
Measuring equipment : TOPCON BM-5A and PR-650

* Ta = 25 ± 2 °C, V_{DD}=3.3V, fv= 60Hz, f_{DCLK}= 65MHz, I_L = 6.0 mA

Item		Symbol	Condition	Min.	Typ.	Max	Unit	Note
Contrast Ratio (5 Points)		CR	Normal Viewing Angle ϕ = 0 θ = 0	300	-	-	-	(1), (2), (5)
Response Time at Ta (Rising + Falling)		T_{RT}		-	25	35	msec	(1), (3)
Average Luminance of White (5 Points)		$Y_{L,AVE}$		150	185	-	cd/m^2	I_L=6.0mA (1), (4)
Color Chromaticity (CIE)	Red	R_X		0.575	0.595	0.615	-	(1), (5) PR-650
		R_Y		0.309	0.329	0.349		
	Green	G_X		0.300	0.320	0.340		
		G_Y		0.523	0.543	0.563		
	Blue	B_X		0.133	0.153	0.173		
		B_Y		0.110	0.130	0.150		
	White	W_X		0.295	0.315	0.335		
		W_Y		0.310	0.330	0.350		
Viewing Angle	Hor.	θ_L	CR ≥ 10 At center	40	45		Degrees	(1), (5) BM-5A
		θ_H		40	45			
	Ver.	ϕ_H		10	20			
		ϕ_L		30	40			
	Hor.	θ_L	CR ≥ 100 At center	(40)	TBD		Degrees	
		θ_H		(40)	TBD			
	Ver.	ϕ_H		(20)	TBD			
		ϕ_L		(20)	TBD			
13 Points White Variation		δ_L		-	-	1.5 (35%)	-	(6)

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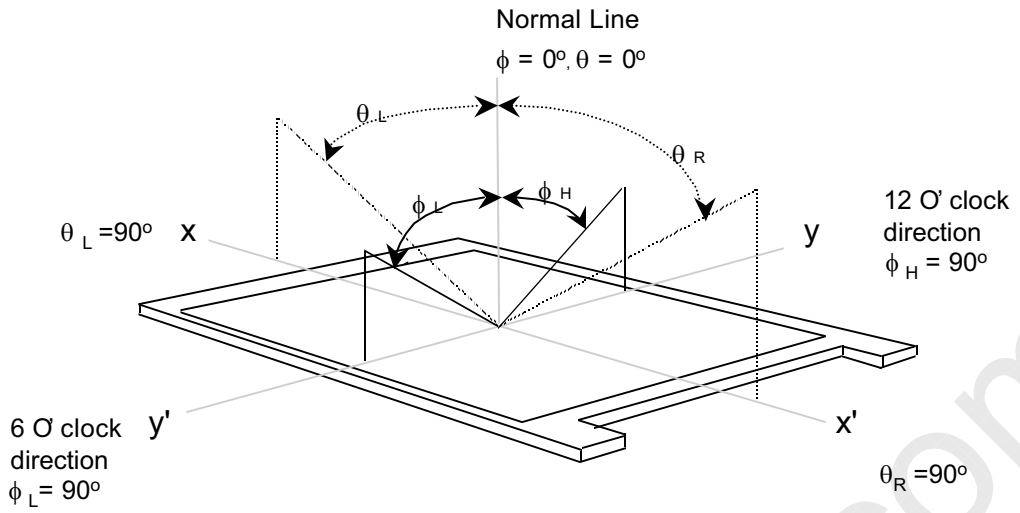
Note 1) Definition of Viewing Angle : Viewing angle range(10 ≤ C/R)

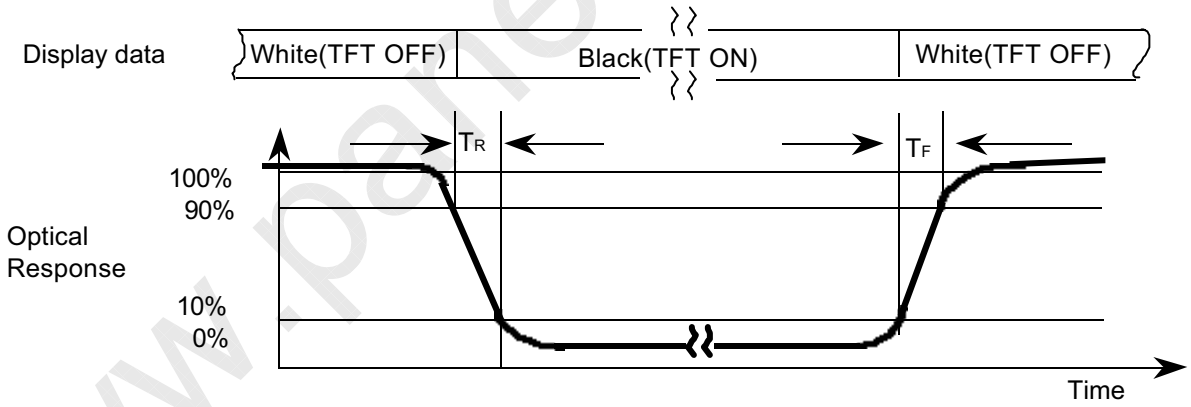


Note 2) Definition of Contrast Ratio (CR) : Ratio of gray max (Gmax) ,gray min (Gmin) at 5 points(4, 5, 7, 9, 10)

$$CR = \frac{CR(4) + CR(5) + CR(7) + CR(9) + CR(10)}{5}$$

Points : ④ , ⑤ , ⑦ , ⑨ , ⑩ at the figure of Note (6).

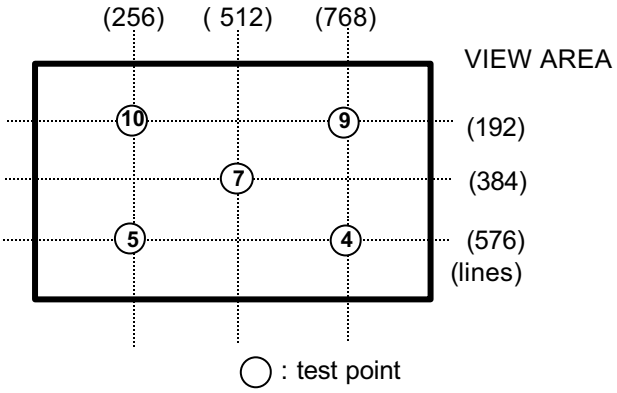
Note 3) Definition of Response time :



Note 4) Definition of Average Luminance of White : measure the luminance of white at 5 points.

Average Luminance of White ($Y_{L,AVE}$)

$$Y_{L,AVE} = \frac{Y_{L4} + Y_{L5} + Y_{L7} + Y_{L9} + Y_{L10}}{5}$$



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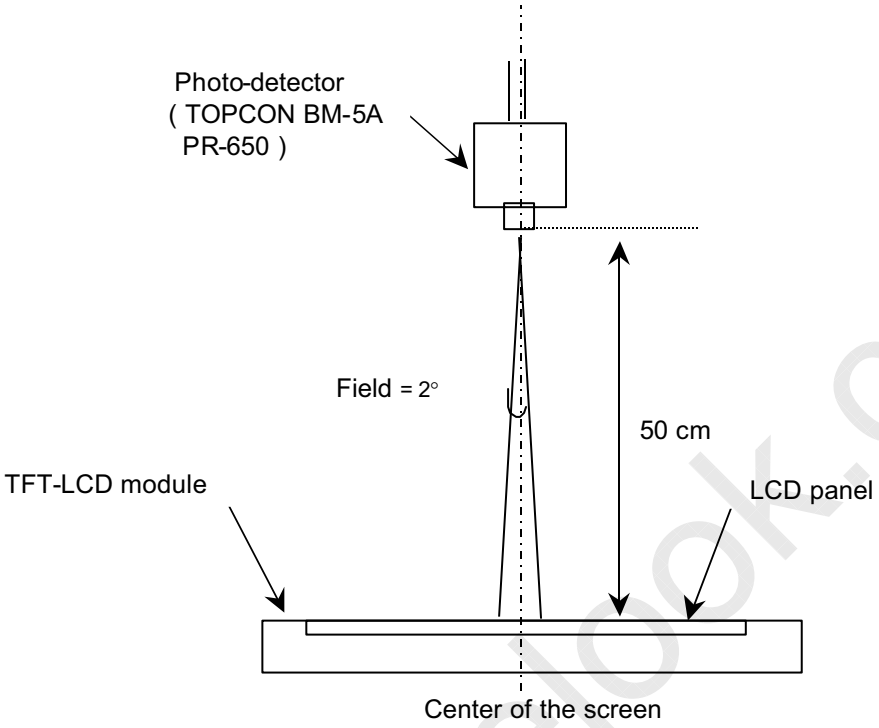
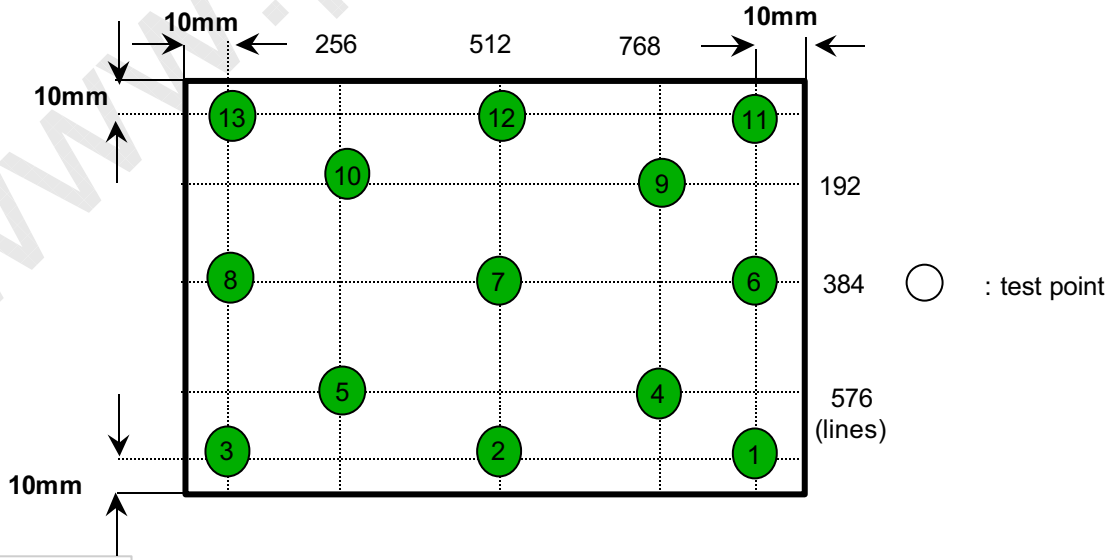
Note 5) After stabilizing and leaving the panel alone at a given temperature for 30 min , the measurement should be executed. Measurement should be executed in a stable, windless,and dark room.
30 min after lighting the backlight. This should be measured in the center of screen.
Lamp current : 6.0mA (Inverter : SIC-130T)
Environment condition : Ta = 25 ± 2 °C



[Optical characteristics measurement setup]

Note 6) Definition of 13 points white variation (δ_L), CR variation(C_{VER}) [① ~ ⑬]

$$\delta_L = \frac{\text{Maximum luminance of 13 points}}{\text{Minimum luminance of 13 points}}$$



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3. ELECTRICAL CHARACTERISTICS

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3.1 TFT LCD MODULE

Ta= 25 ± 2°C

Item		Symbol	Min.	Typ.	Max.	Unit	Note
Voltage of Power Supply		V_{DD}	3.0	3.3	3.6	V	
Differential Input Voltage for LVDS Receiver Threshold	High	V_{IH}	-	-	+100	mV	V_{CM} = +1.2V
	Low	V_{IL}	-100	-	-	mV	
Vsync Frequency		fv	-	60	-	Hz	
Hsync Frequency		f_H	-	48.3	-	KHz	
Main Frequency		f_{DCLK}	-	65	-	MHz	
Rush Current		I_{RUSH}	-	-	1.5	A	(4)
Current of Power Supply	White	I_{DD}	-	TBD	-	mA	(2),(3)*a
	Mosaic		-	TBD	-	mA	(2),(3)*b
	V. stripe		-	TBD	TBD	mA	(2),(3)*c

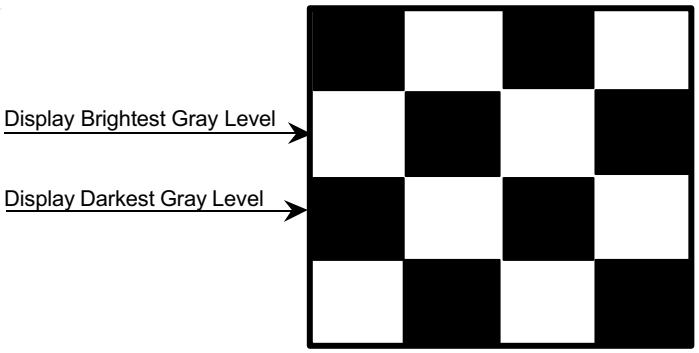
Note (1) Display data pins and timing signal pins should be connected.(GND = 0V)

(2) f_V = 60Hz, f_{DCLK} = 65MHZ, V_{DD} = 3.3V , DC Current.

(3) Power dissipation pattern

*a) White Pattern

*b) Mosaic Pattern



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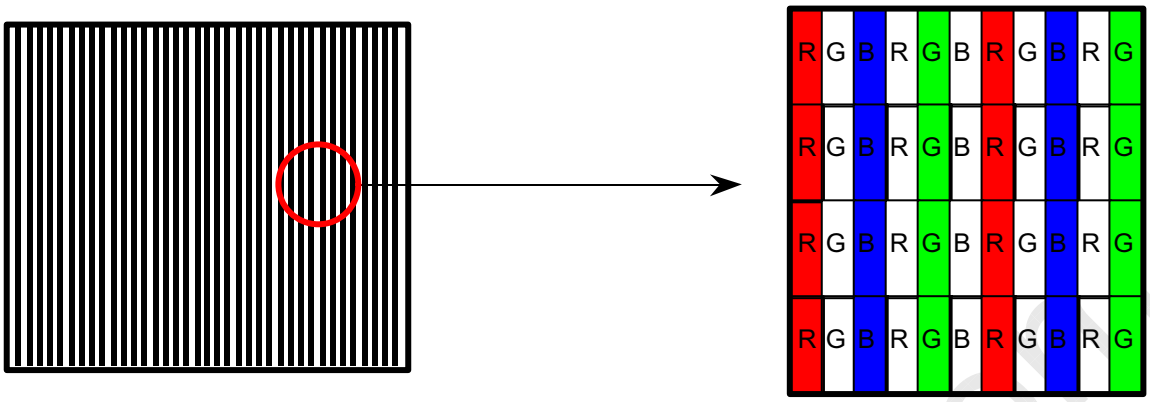
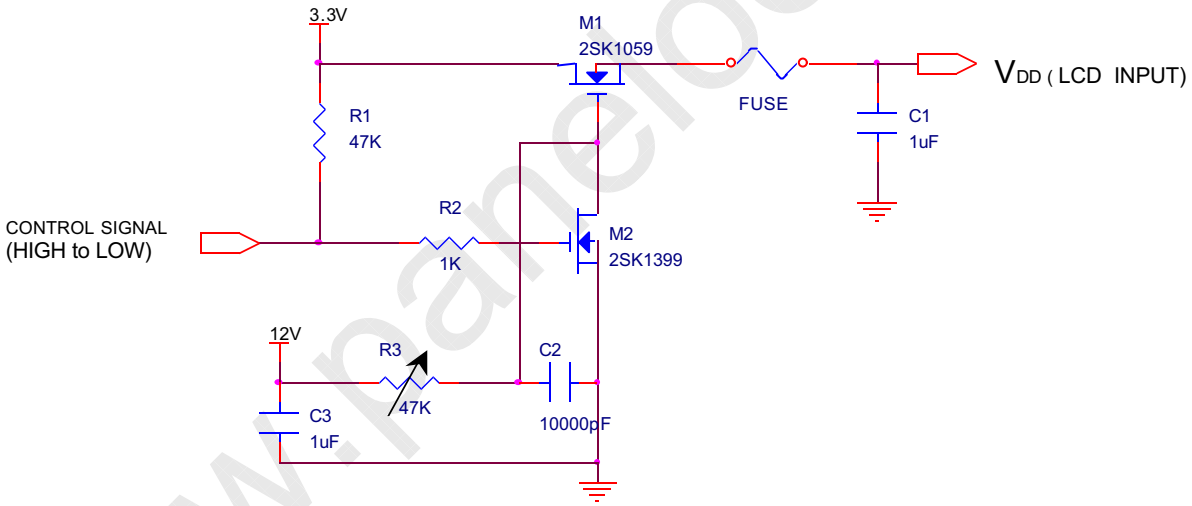
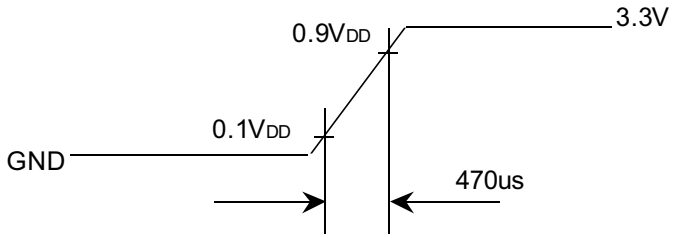
*c) 1dot Vertical stripe pattern



4) Rush current measurement condition



VDD rising time is 470us



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3.2 BACK-LIGHT UNIT

The backlight system is an edge-lighting type with a single CCFT (Cold Cathode Fluorescent Tube).
The characteristics of a single lamp are shown in the following table.

- INVERTER : SEM SIC 130T

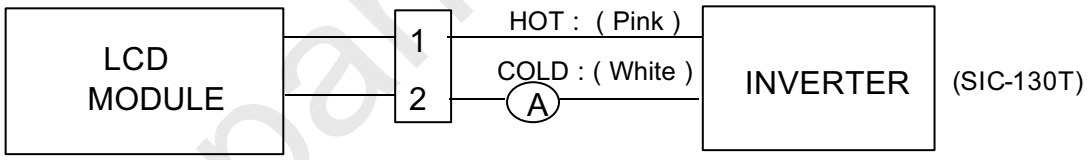
Ta= 25 ± 2 °C

Item	Symbol	Min.	Typ.	Max.	Unit	Note
Lamp Current	I_L	3.0	5.0	6.0	mArms	(1)
Lamp Voltage	V_L	-	700	-	Vrms	I_L= 6.5mA
Frequency	f_L	50	-	65	KHz	(2)
Power Consumption	P_L		4.2		W	(3) I_L= 6.0mA
Operating Life Time	Hr	10,000			Hour	(4)
Startup Voltage	V_S			1095	Vrms	25°C, (5)
				1315	Vrms	0°C, (5)
Lamp Start-up time	T_S	-	-	0.5	sec	

Note) The waveform of the inverter output voltage must be area symmetric and the design of the inverter must have specifications for the modularized lamp.

The performance of the backlight, for example life time or brightness, is much influenced by the characteristics of the DC-AC inverter for the lamp. So all the parameters of an inverter should be carefully designed so as not to produce too much leakage current from high-voltage output of the inverter.
When you design or order the inverter, please make sure that a poor lighting caused by the mismatch of the backlight and the inverter(miss lighting, flicker, etc.) never occur. When you confirm it, the module should be operated in the same condition as it is installed in your instrument.

Note (1) Lamp current is measured with a high frequency current meter as shown below.



(2) Lamp frequency may produce interference with horizontal synchronous frequency and this may cause line flow on the display. Therefore lamp frequency should be detached from the horizontal synchronous frequency and its harmonics as far as possible in order to avoid interference.

(3) Refer to $I_L \times V_L$ to calculate.

(4) Life time (Hr) of a lamp can be defined as the time in which it continues to operate under the condition Ta= 25 ± 2 °C and I_L = 6.0 mArms until one of the following event occurs.

1. When the brightness becomes 50% or lower than the original.
2. When the Effective ignition length becomes 80% or lower than the original value.
   (Effective ignition length is defined as an area that has less than 70% brightness compared to the brightness in the center point.)

(5) The inverter open voltage - this voltage should be measured after ballast capacitor- have to be larger than the lamp startup voltage, otherwise backlight may has blinking for a moment after turns on or not be turned on.
If an inverter has shutdown function it should keep its open voltage for longer than 1 second even if lamp connector open.

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3.3 Inverter

Inverter Manufacturer : TBD(AMBIT, SUMIDA, DELTA) Ta = 25 ± 2°C

Item		Min.	Typ.	Max	Unit	Note
Input Voltage (Vin)		7.5	14.4	21.0	V	
Open Circuit Voltage		1400	-	1800	Vrms	I_L = 6.0mArms
Lamp Current (Duty Cycle)		20 @SMB_DAT FFH	-	100 @SMB_DAT 00H I_L=6.5mArms	%	Vin=14.4V
Efficiency	Optical	20	-	-	nit / W	After 30min turn on at the center of LCD Vin=14.4V @6.5mA
	Electrical	-	85	-	%	
Operating Frequency		45	55	65	kHz	SMB_DAT=00H
Input voltage ripple		-	-	0.5	V	Peak-to-peak
Input Power Consumption		-	4.7	5.5	W	Vin=14.4V Iout = 6.5mArms
In-rush current		-	-	1.5	A	
Shutdown time		0.6	1.0	1.4	sec	
Start-up time		-	-	0.1	sec	(1)

Note)
(1) Inverter start-up time
(2) Efficiency should be calculated as below formulation.
Optical efficiency = output Brightness(nits) / Input power(watt)
Electrical efficiency = output power / input power

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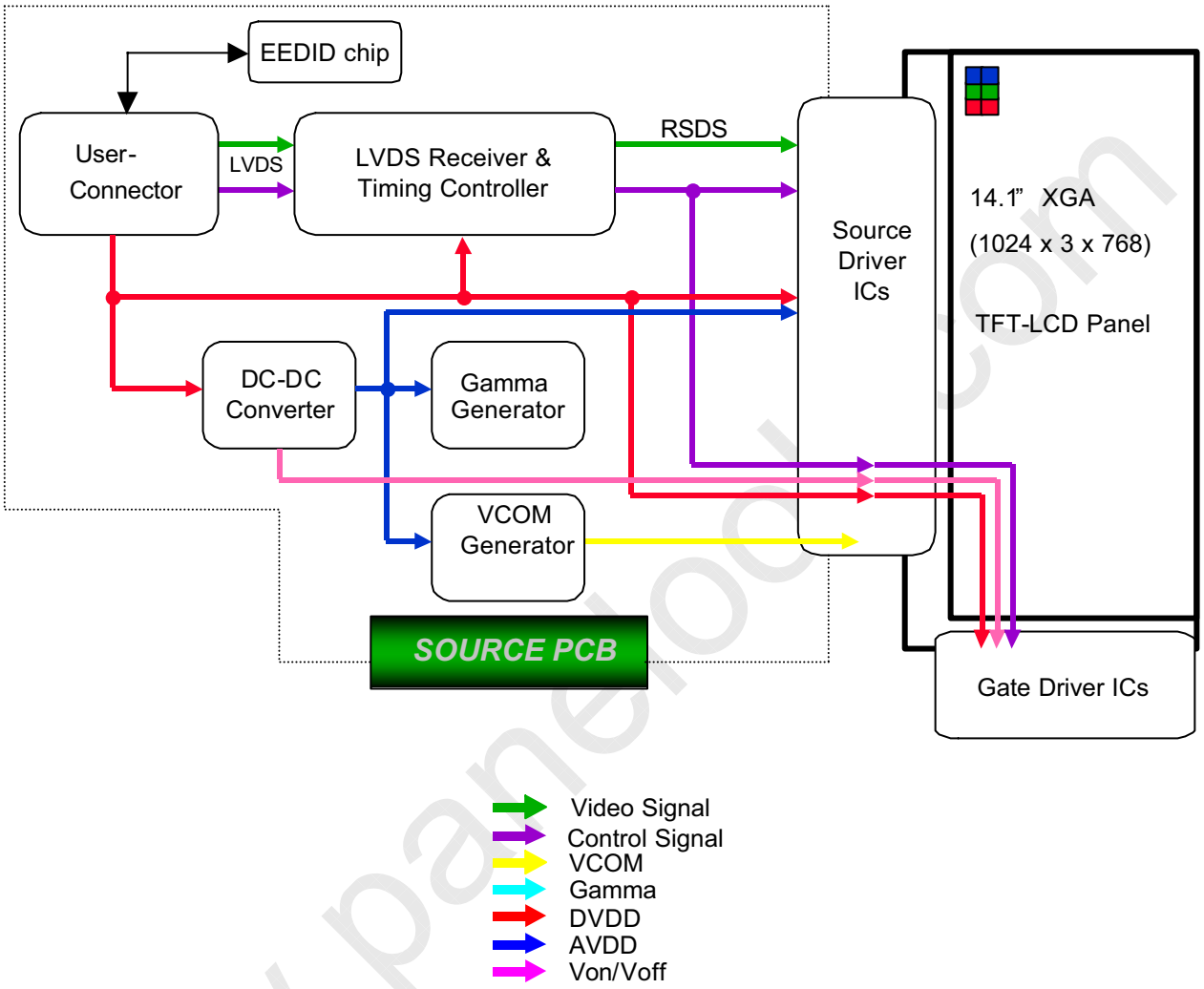
4. BLOCK DIAGRAM

4.1 TFT LCD Module



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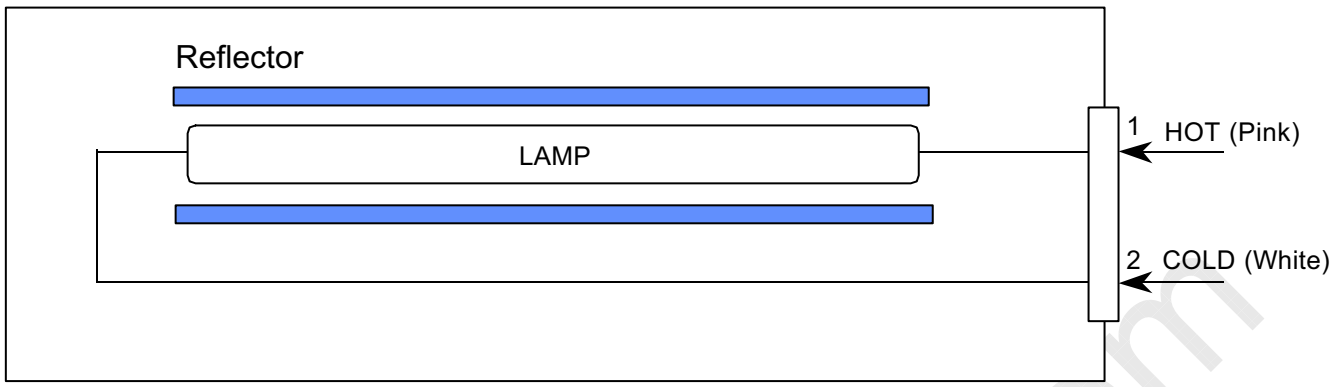
4.2 BACKLIGHT UNIT

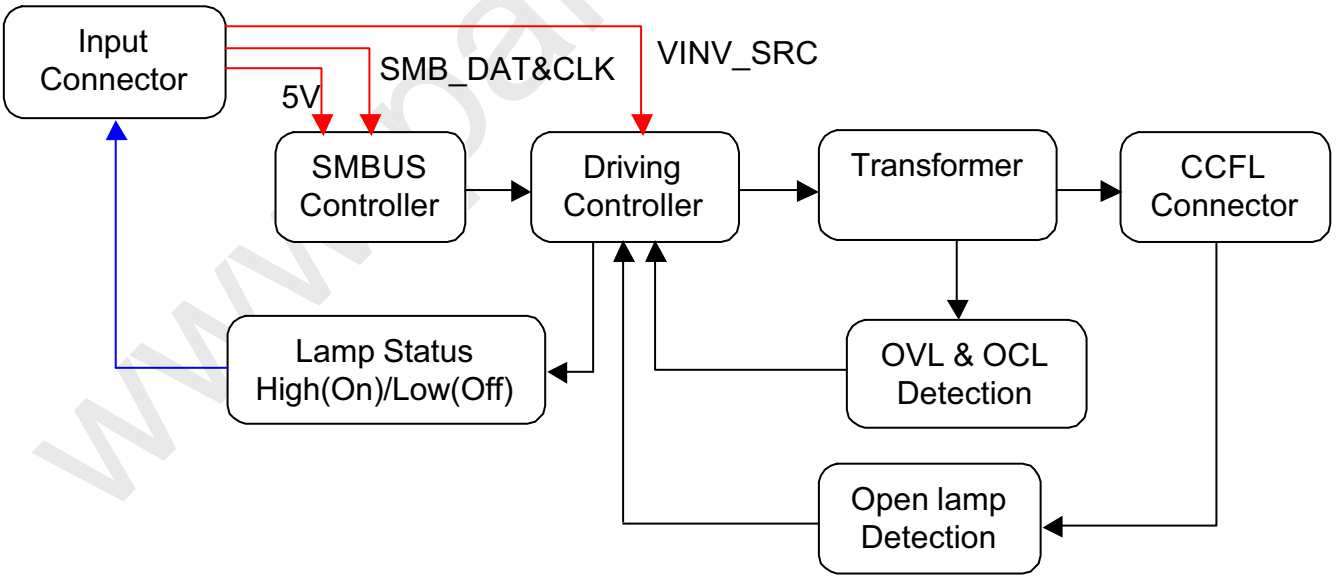


Note) The output of the inverter may change according to the material of the reflector.

4.3 Inverter UNIT

Input Connector : Honda, LVC-D20SFYG

Lamp Connector : JST, SM02B-BHSS-1-TB



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5. INPUT TERMINAL PIN ASSIGNMENT

5.1. Input Signal & Power (LVDS, Connector : JAE FI-XB30SRL-HF11 or compatible)
Mating Connector : JAE FI-X30CL or compatible)

No.	Symbol	Function	Polarity	Remarks
1	VSS	Ground		
2	VDD	POWER SUPPLY +3.3V		
3	VDD	POWER SUPPLY +3.3V		
4	VEEDID	DDC 3.3V Power		
5	BIST	Panel BIST enable		
6	CLKEDID	DDC Clock		
7	DATAEDID	DDC data		
8	RxIN0-	LVDS Differential Data INPUT (R0-R5,G0)	Negative	
9	RxIN0+	LVDS Differential Data INPUT (R0-R5,G0)	Positive	
10	GND	Ground		
11	RxIN1-	LVDS Differential Data INPUT (G1-G5,B0-B1)	Negative	
12	RxIN1+	LVDS Differential Data INPUT (Odd G1-G5,B0-B1)	Positive	
13	GND	Ground		
14	RxIN2-	LVDS Differential Data INPUT (B2-B5,Sync,DE)	Negative	
15	RxIN2+	LVDS Differential Data INPUT (B2-B5,Sync,DE)	Positive	
16	GND	Ground		
17	RxCLK-	LVDS Differential Data INPUT	Negative	
18	RxCLK+	LVDS Differential Data INPUT	Positive	
19	GND	Ground		
20	NC	No connection		
21	NC	No connection		
22	NC	No connection		
23	NC	No connection		
24	NC	No connection		
25	NC	No connection		
26	NC	No connection		
27	NC	No connection		
28	NC	No connection		
29	NC	No connection		
30	NC	No connection		

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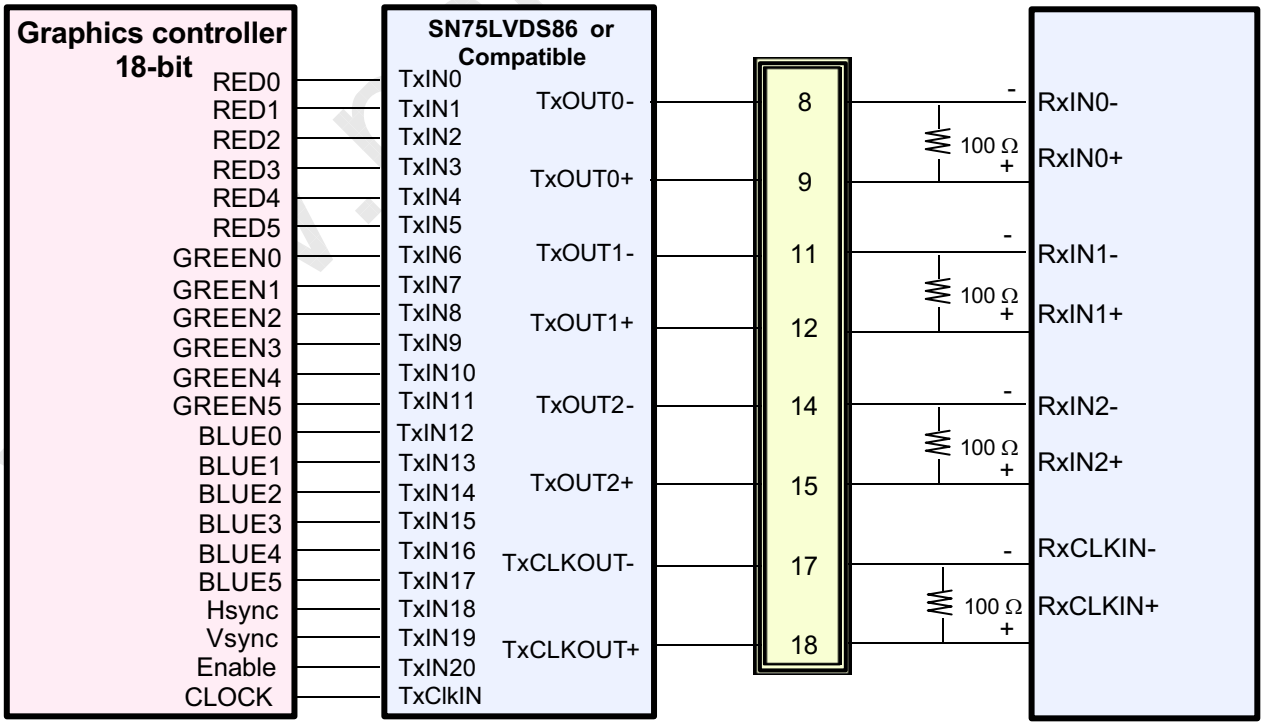
5.2 LVDS Interface : Transmitter DS90CF363 or Compatible

LVDS for Odd pixel

Pin No.	Name	RGB Signal	Pin No.	Name	RGB Signal
44	TxIN0	RO0	12	TxIN11	GO5
45	TxIN1	RO1	13	TxIN12	BO0
47	TxIN2	RO2	15	TxIN13	BO1
48	TxIN3	RO3	16	TxIN14	BO2
1	TxIN4	RO4	18	TxIN15	BO3
3	TxIN5	RO5	19	TxIN16	BO4
4	TxIN6	GO0	20	TxIN17	BO5
6	TxIN7	GO1	22	TxIN18	Hsync
7	TxIN8	GO2	23	TxIN19	Vsync
9	TxIN9	GO3	25	TxIN20	DE
10	TxIN10	GO4	26	TxCLK IN	Clock

LVDS INTERFACE



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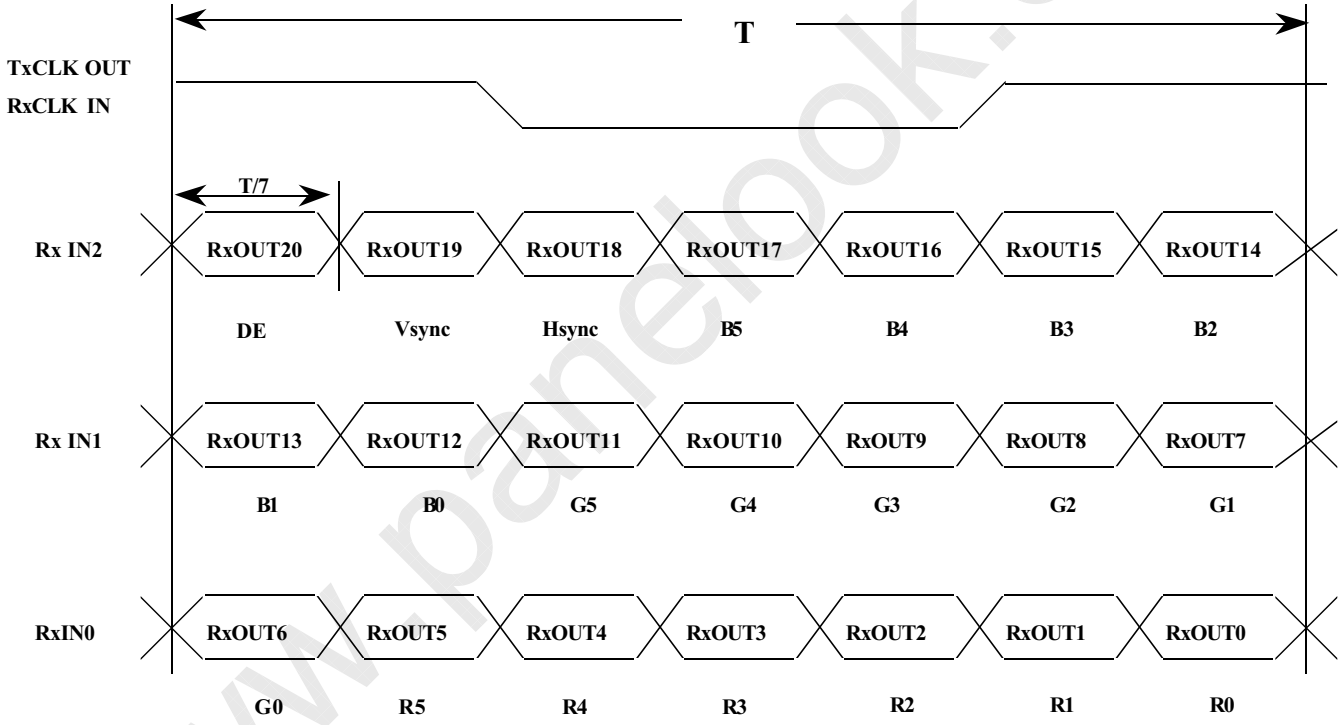
5.3 BACK LIGHT UNIT

Connector : JST BHSR - 02VS -1
Mating Connector : SM02B-BHSS-1(JST)

Pin NO.	Symbol	Color	Function
1	HOT	Pink	High Voltage
2	COLD	White	Low Voltage

5.4 Timing Diagrams of LVDS For Transmission

LVDS Receiver : Integrated T-CON



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5.5 Input Signals, Basic Display Colors and Gray Scale of Each Color

Color	Display	Data Signal																		Gray Scale Level
		Red						Green						Blue						
		R0	R1	R2	R3	R4	R5	G0	G1	G2	G3	G4	G5	B0	B1	B2	B3	45	B5	
Basic Colors	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	-
	Blue	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	-
	Green	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0	-
	Cyan	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	-
	Red	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	-
	Magenta	1	1	1	1	1	1	0	0	0	0	0	0	1	1	1	1	1	1	-
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	-
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	-
Gray Scale Of Red	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R0
	Dark	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R1
	↑	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R2
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	R3~R60
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	
	↓	1	0	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	R61
	Light	0	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	R62
	Red	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	R63
Gray Scale Of Green	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	G0
	Dark	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	G1
	↑	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	G2
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	G3~G60
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	
	↓	0	0	0	0	0	0	1	0	1	1	1	1	0	0	0	0	0	0	G61
	Light	0	0	0	0	0	0	0	1	1	1	1	1	0	0	0	0	0	0	G62
	Green	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0	G63
Gray Scale Of Blue	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	B0
	Dark	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	B1
	↑	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	B2
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	B3~B60
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	
	↓	0	0	0	0	0	0	0	0	0	0	0	0	1	0	1	1	1	1	B61
	Light	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	B62
	Blue	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	B63

Note 1) Definition of gray :
Rn: Red gray, Gn: Green gray, Bn: Blue gray (n=gray level)
Note 2)Input signal: 0 =Low level voltage, 1=High level voltage

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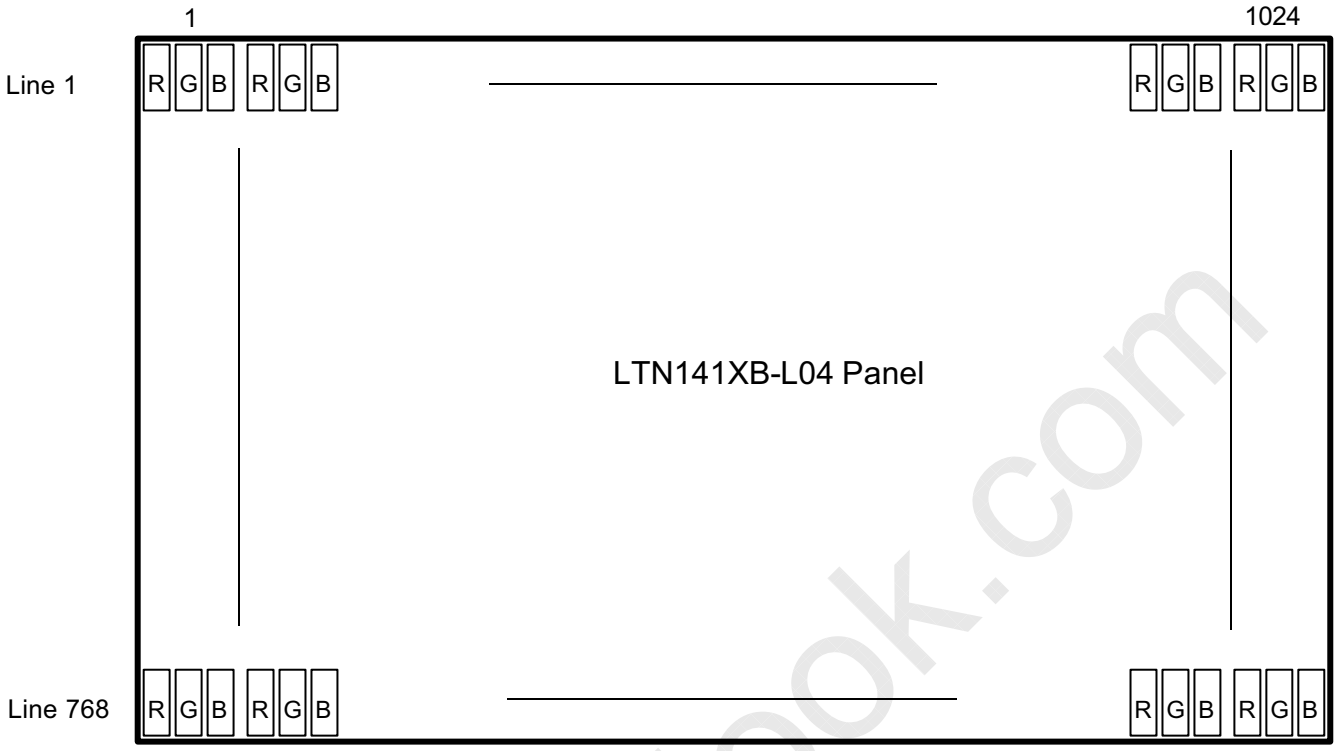
5.6 Pixel Format in the display



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5.7 Inverter signals & power

Inverter Connector : Honda, LVC-D20SFYG

Pin No.	Symbol	Voltage	Comments
1	INV_SRC	7.5V to 21V	This power rail should be used as a power rail to drive the back-light DC-AC converter.
2	INV_SRC	7.5V to 21V	This power rail should be used as a power rail to drive the back-light DC-AC converter.
3	INV_SRC	7.5V to 21V	This power rail should be used as a power rail to drive the back-light DC-AC converter.
4	NC	-	No Connection
5	GND	0V	Ground
6	5VSUS	5V	This should be used as power source for the control circuitry on the inverter.
7	5VALW	5V	This should be used as power source that stores the brightness/contrast values & the circuit that interfaces with SMB_CLK & SMB_DAT.
8	GND	0V	Ground
9	SMB_DAT	-	SMBUS interface for sending brightness & contrast information to the inverter/panel
10	SMB_CLK	-	SMBUS interface for sending brightness & contrast information to the inverter/panel
11	GND	0V	Ground
12	FPBACK	-	Control signal input into the inverter to turn ON or OFF Lamp. (1 - ON, 0 - OFF)
13	GND	0V	Ground
14	LAMP_STAT	-	Lamp Status Output On (High) / Off (Low) from control chip.
15	NC	-	No Connection
16	NC	-	No Connection
17	NC	-	No Connection
18	NC	-	No Connection
19	NC	-	No Connection
20	NC	-	No Connection

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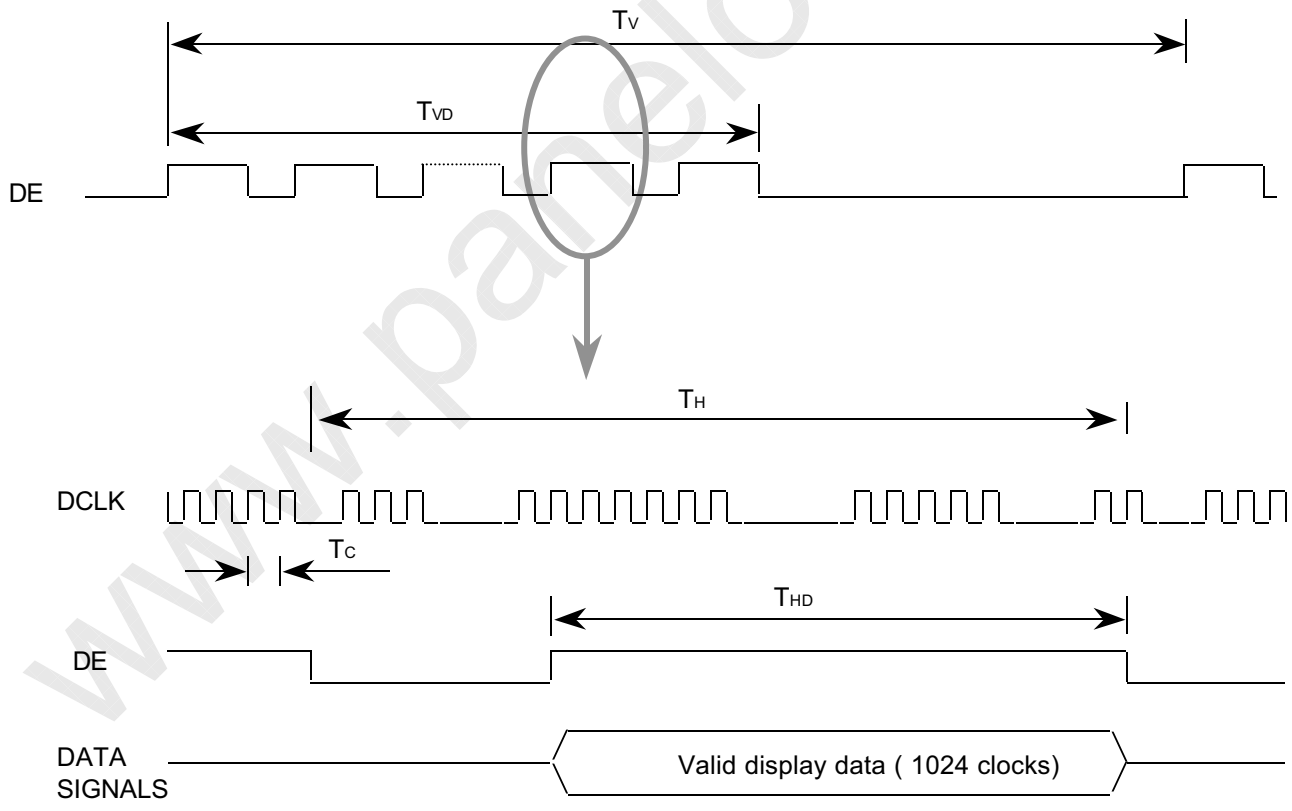
6. INTERFACE TIMING

6.1 Timing Parameters

Signal	Item	Symbol	Min.	Typ.	Max.	Unit	Note
Frame Frequency	Cycle	TV	-	806	-	Lines	
Vertical Active Display Term	Display Period	TVD	-	768	-	Lines	
One Line Scanning Time	Cycle	TH	-	1344	-	Clocks	
Horizontal Active Display Term	Display Period	THD	-	1024	-	Clocks	

6.2 Timing diagrams of interface signal



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6.3 Power ON/OFF Sequence

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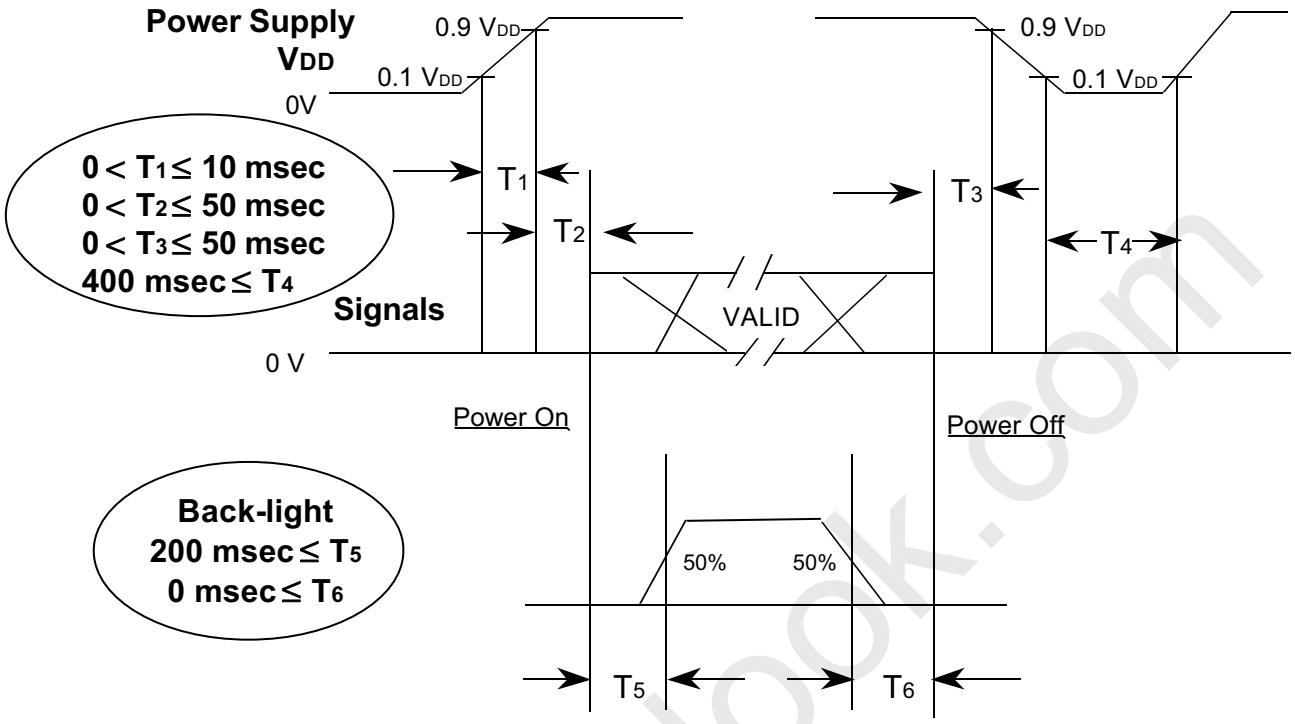
: To prevent a latch-up or DC operation of the LCD module, the power on/off sequence should be as the diagram below.



Power ON/OFF Sequence

T1 : Vdd rising time from 10% to 90%
T2 : The time from Vdd to valid data at power ON.
T3 : The time from valid data off to Vdd off at power Off.
T4 : Vdd off time for Windows restart
T5 : The time from valid data to B/L enable at power ON.
T6 : The time from valid data off to B/L disable at power Off.

NOTE.

(1) The supply voltage of the external system for the module input should be the same as the definition of V_{DD}.

(2) Apply the lamp voltage within the LCD operation range. When the back-light turns on before the LCD operation or the LCD turns off before the back-light turns off, the display may momentarily become white.

(3) In case of V_{DD} = off level, please keep the level of input signals on the low or keep a high impedance.

(4) T4 should be measured after the module has been fully discharged between power off and on period.

(5) Interface signal shall not be kept at high impedance when the power is on.

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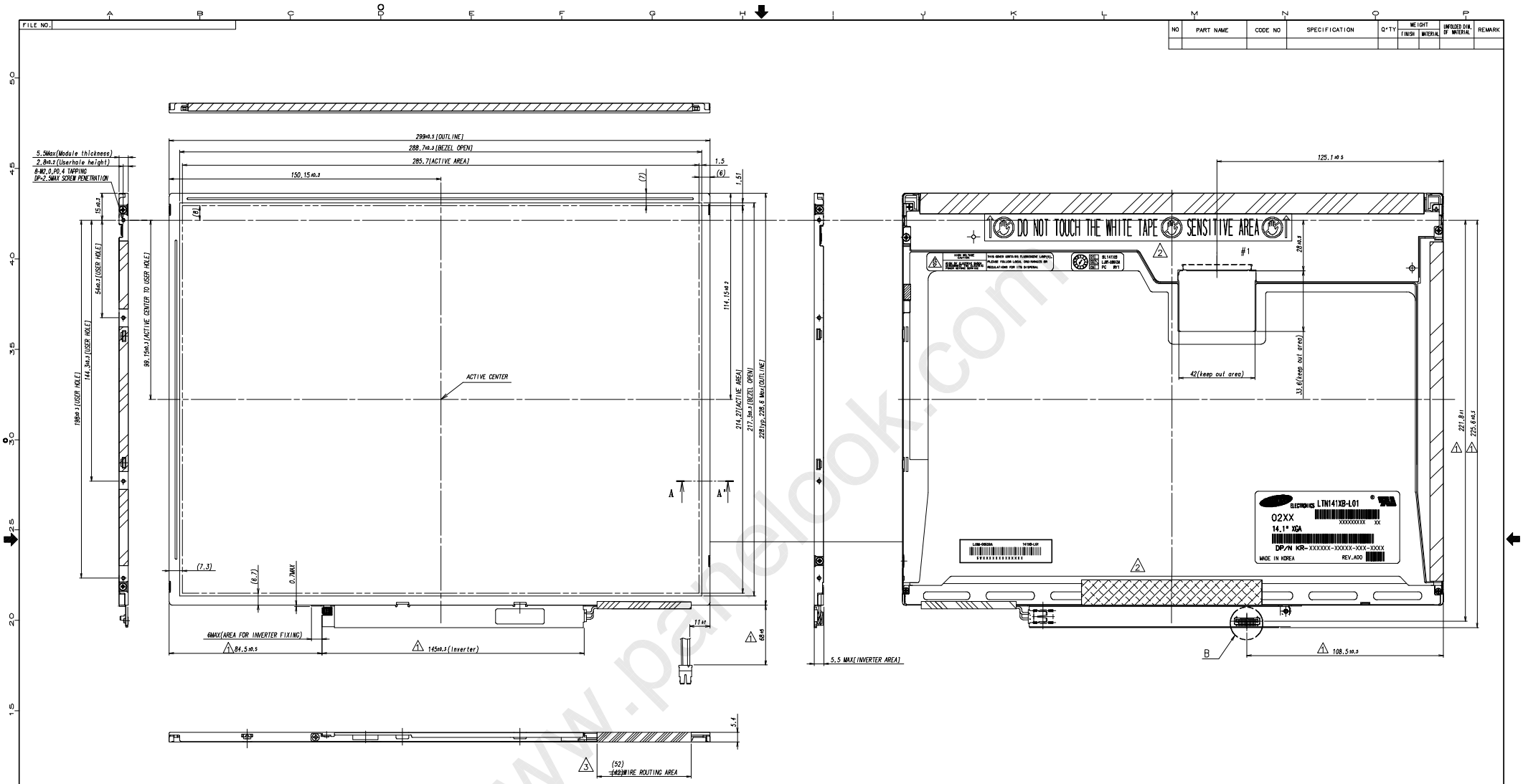

<USERHOLE PART>
SECTION A-A' (S=5/1)

DETAIL B(S=2/1)

* NOTE

1. SIGNAL INTERFACE CONNECTOR TO BE SPECIFIED AS BELOW.
   - MAKER : JAE
   △1 - INPUT MATCHING CONNECTOR : JAE FI-XB30SRL-HF11
2. CCFT CONNECTOR FOR BACKLIGHT TO BE SPECIFIED AS BELOW.
   - MAKER : JST
   - PART NO : BHSR-02VS-1
   - LAMP DIAMETER : ø1.8
3. CALIFERS MEASURING FORCE :530 ± 150 gf
4. ALLOWED DEPTH OF USERHOLE SCREW INSERTION IS 2.5mm MAX

NO	PART NAME	CODE NO	SPECIFICATION	Q'TY	WEIGHT FINISH	WEIGHT MATERIAL	UNFOLDED DIM. OF MATERIAL	REMARK

REVISION				
003	02.08.08	△3 CHANGE THE LENGTH OF TAPE-WIRE	CUSTOMER REQUEST	S.C.JEONG
002	02.05.20	△2 ADDING THE LABEL-WARNING & AI-TAPE	CUSTOMER REQUEST & FOR QUALITY	S.H.PARK
REV	DATE	DESCRIPTION OF REVISION	REASON	CHG'D BY

GENERAL TOLERANCE			
STEP	LEVEL 1	LEVEL 2	LEVEL 3
0 < X ≤ 4	±0.05	±0.1	±0.2
4 < X ≤ 16	±0.08	±0.15	±0.3
16 < X ≤ 64	±0.12	±0.25	±0.5
64 < X ≤ 256	±0.25	±0.4	±0.8

REV	DATE	DESCRIPTION OF REVISION	DRA'N BY	DES'D BY	CHK'D BY	APP'D BY
001	01.11.10	△1 CHANGE THE INVERTER & CONNECTOR		S.H.PARK	S.H.LEE	Y.B.CHU
UNIT	mm			01.07.27	01.07.27	01.07.28
SCALE	1/1					
TOLERANCE	LEVEL 3					

SAMSUNG ELECTRONICS

CUSTOMER REQUEST		S.H.PARK
REASON		CHG'D BY
MODEL NAME	LTN141XB-L01	
PART/SHEET NAME	OUTLINE DIMENSION	SHEET 1/1
CODE NO.		VER. 003
SPEC. NO		

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8. PACKING

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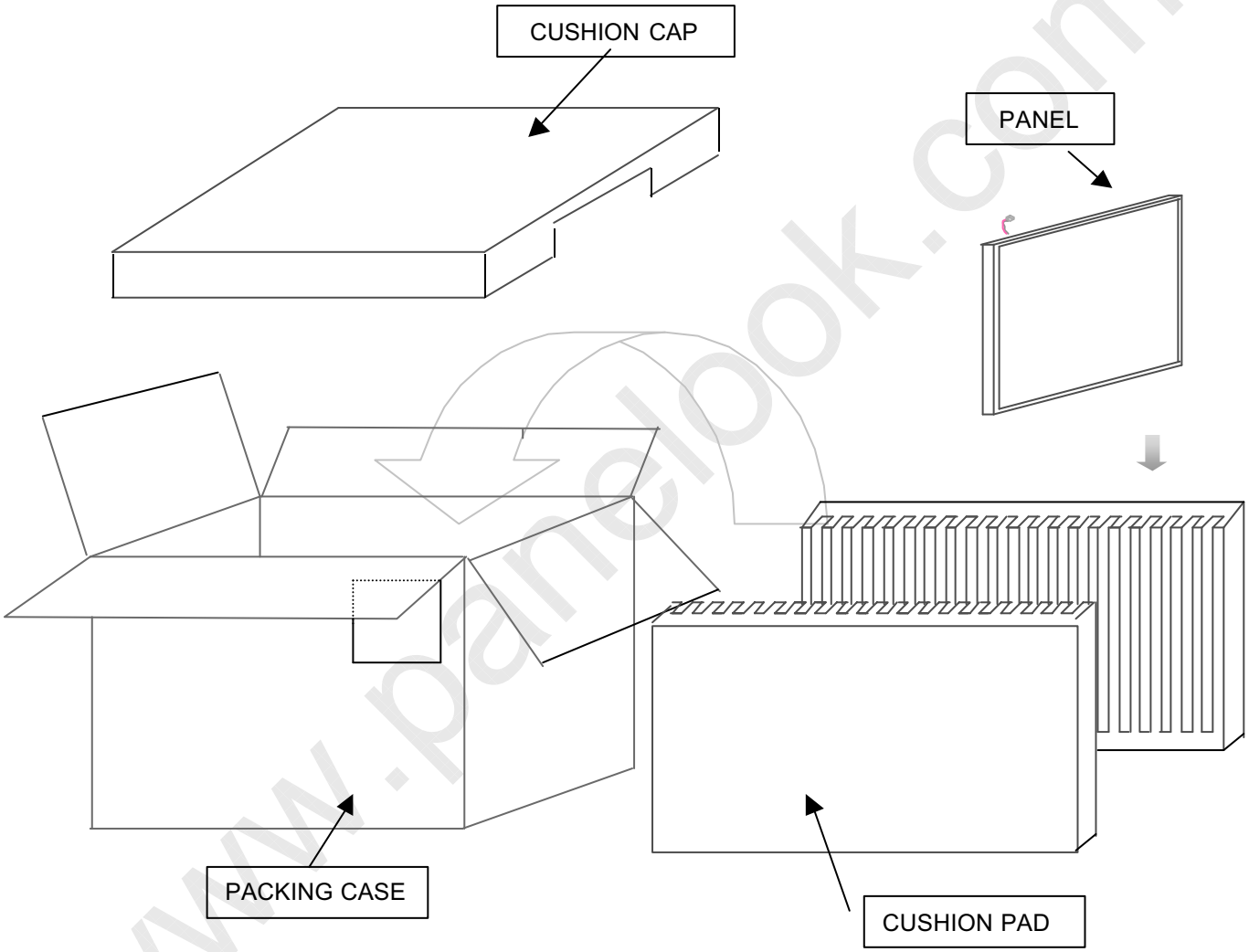
1. CARTON(Internal Package)

(1) Packing Form

Corrugated Cardboard box and EPS(Expandable Polystyrene) form as shock absorber

(2) Packing Method



Note 1)Total Weight : Approximately 14 kg

2) Acceptance number of panel : 20 sets

3) Carton size : 504(W)×370(D)×324(H)

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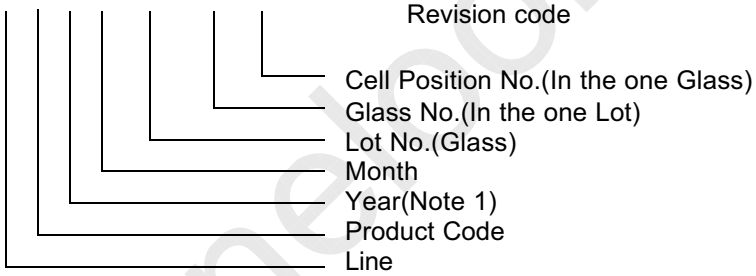
No	Part name	Quantity
1	Static electric protective sack	20
2	Packing case (Inner box) included shock absorber	1 set
3	Pictorial marking	2 pcs
4	Carton	1 set

9. MARKINGS & OTHERS

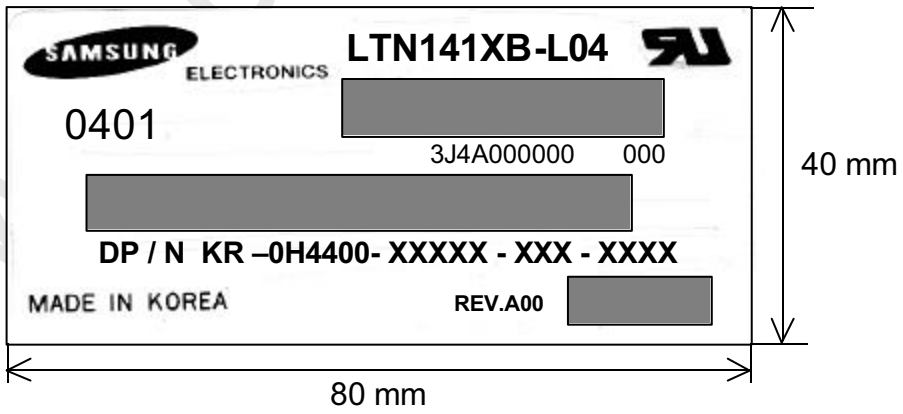
A nameplate bearing followed by is affixed to a shipped product at the specified location on each product.

(1)Parts number : LTN141XB-L04
(2)Revision : Three letters
(3)Lot number : 3 J 4 A XXX XX X XXX



NOTE 1). This code indicating year is omitted in the products of KIHENG site.

(5) Nameplate Indication



Parts name : LTN141XB - L04
Lot number : 3J4A000000
Inspected work week : 0401
DP/N : Dell Part Number (“**0H4400**” is for 141XB-L01)
REV.A00 : Product Revision Code

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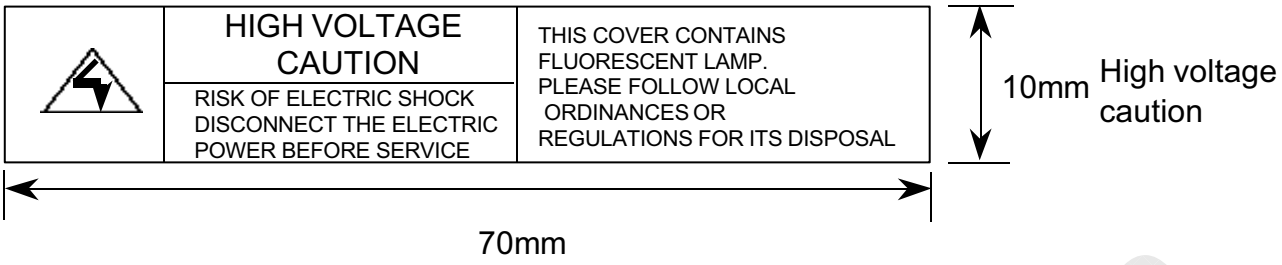
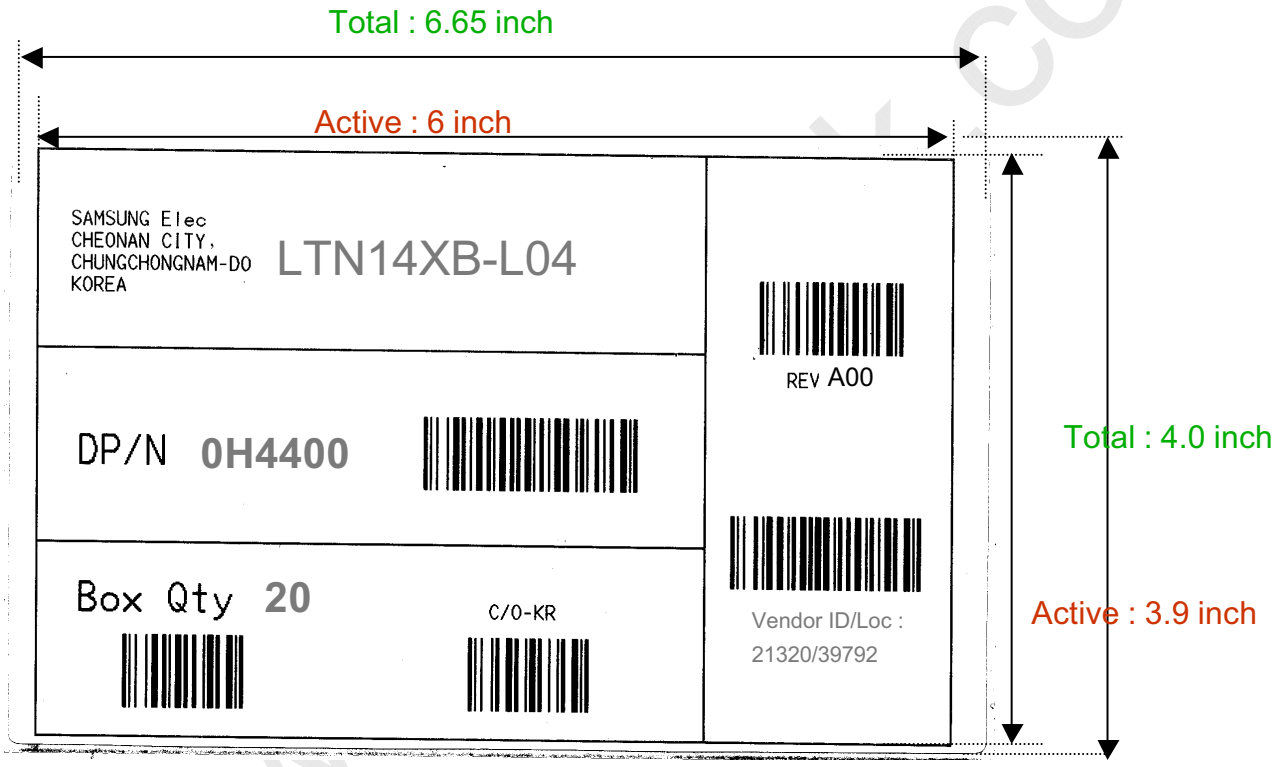
High voltage caution label



(6) Packing box attach



(7) Packing box Marking : Samsung TFT-LCD Brand Name

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10. GENERAL PRECAUTIONS

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1. Handling

(a) When the module is assembled, It should be attached to the system firmly using every mounting holes. Be careful not to twist and bend the modules.

(b) Refrain from strong mechanical shock and / or any force to the module. In addition to damage, this may cause improper operation or damage to the module and CCFT back-light.

(c) Note that polarizers are very fragile and could be easily damaged. Do not press or scratch the surface harder than a HB pencil lead.

(d) Wipe off water droplets or oil immediately. If you leave the droplets for a long time, Staining and discoloration may occur.

(e) If the surface of the polarizer is dirty, clean it using some absorbent cotton or soft cloth.

(f) The desirable cleaners are water, IPA (Isoprophyl Alcohol) or Hexane.
Do not use Ketone type materials(ex. Acetone), Ethyl alcohol, Toluene, Ethyl acid or Methyl chloride. It might permanent damage to the polarizer due to chemical reaction.

(g) If the liquid crystal material leaks from the panel, it should be kept away from the eyes or mouth . In case of contact with hands, legs or clothes, it must be washed away thoroughly with soap.

(h) Protect the module from static , it may cause damage to the C-MOS Gate Array IC.

(i) Use fingerstalls with soft gloves in order to keep display clean during the incoming inspection and assembly process.

(j) Do not disassemble the module.

(k) Do not pull or fold the lamp wire.

(l) Do not adjust the variable resistor which is located on the back side.

(m) Protection film for polarizer on the module shall be slowly peeled off just before use so that the electrostatic charge can be minimized.

(n) Pins of I/F connector shall not be touched directly with bare hands.

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2. STORAGE

(a) Do not leave the module in high temperature, and high humidity for a long time. It is highly recommended to store the module with temperature from 0 to 35 °C and relative humidity of less than 70%.

(b) Do not store the TFT-LCD module in direct sunlight.

(c) The module shall be stored in a dark place. It is prohibited to apply sunlight or fluorescent light during the store.

3. OPERATION

(a) Do not connect,disconnect the module in the " Power On" condition.

(b) Power supply should always be turned on/off by following item 6.3 " Power on/off sequence " .

(c) Module has high frequency circuits. Sufficient suppression to the electromagnetic interference shall be done by system manufacturers. Grounding and shielding methods may be important to minimize the interference.

(d) The cable between the back-light connector and its inverter power supply shall be a minimized length and be connected directly . The longer cable between the back-light and the inverter may cause lower luminance of lamp(CCFT) and may require higher startup voltage (Vs).

(e) The standard limited warranty is only applicable when the module is used for general notebook applications. If used for purposes other than as specified, SEC is not to be held reliable for the defective operations. It is strongly recommended to contact SEC to find out fitness for a particular purpose.

4. OTHERS

(a) Ultra-violet ray filter is necessary for outdoor operation.

(b) Avoid condensation of water. It may result in improper operation or disconnection of electrode.

(c) Do not exceed the absolute maximum rating value. (the supply voltage variation, input voltage variation, variation in part contents and environmental temperature, so on) Otherwise the module may be damaged.

(d) If the module displays the same pattern continuously for a long period of time,it can be the situation when the image " sticks" to the screen.

(e) This module has its circuitry PCB' s on the rear side and should be handled carefully in order not to be stressed.

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